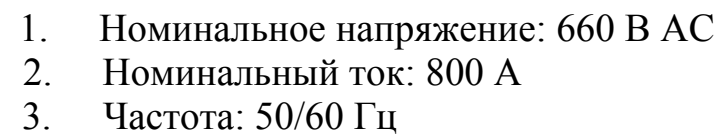
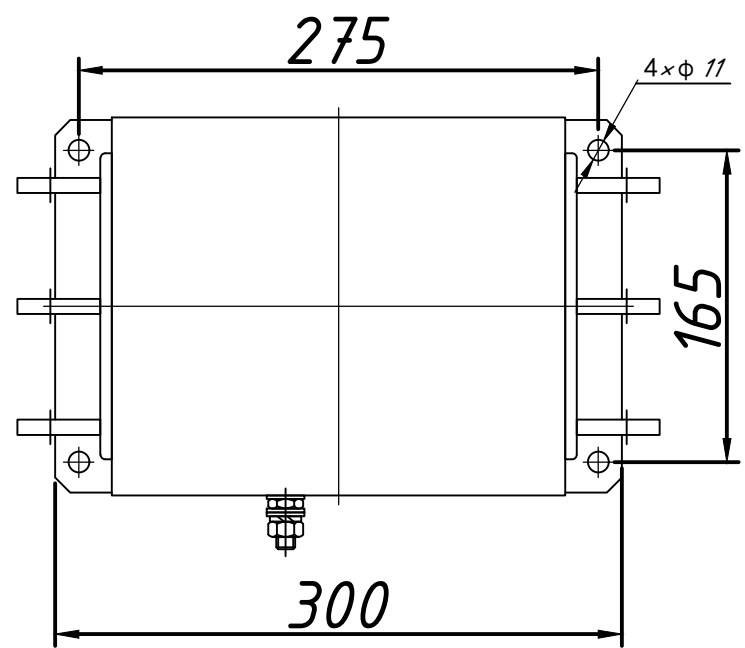


Инв. № подл.	Подп. и дата	Взамен инв. №



The diagram shows a three-port network with input ports L1, L2, and L3, and output ports L1', L2', and L3'. The network is connected to a common ground G. The circuit consists of three horizontal sections, each containing an inductor L. The first section has capacitors Cx in parallel with the input lines. The second section has capacitors Cx and Cy in parallel with the input lines. The third section has capacitors Cx and Cy in parallel with the input lines, and resistors R1 and R2 in parallel with the output lines. The output lines are connected to a common ground G'.

[illegible]